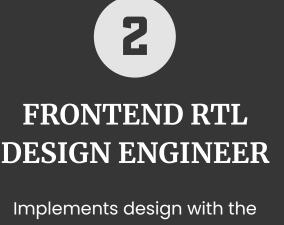
## VARIOUS VLSI ROLES



help of Verilog



## SOC VERIFICATION **ENGINEER**

Verify design's functionality with the help of System Verilog and UVM



## **ENGINEER**

Help customers with Product Adoption



## **CAD ENGINEER**

Help to automate parts of the design flow





Extend technical assistance to the production team, clients and customer





